**Course Syllabus**

Academic year: 2020-2021

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| Institution | University of Petroşani |
| Faculty | Mechanical and Electrical Engineering |
| Field of study | Computers and Information Technology |
| Level | Bachelor |
| Program of study | Computers Engineering |

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| Course | **Logic design** |
| Code | 2CC3OD17 |
| Year of study (semester) | II (I) |
| Number of hours | 84 |
| Number of credits | 6 |
| Professor | Assoc. Prof., Ph.D. PATRASCOIU Nicolae |

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| **No.** | **Topic** |
|  | Numeration systems |
|  | Boolean algebra |
|  | Logical functions |
|  | Analysis and synthesis of combination logic circuits (CLC) |
|  | Implementation of CLC by logic gates |
|  | Encoders and decoders |
|  | Multiplexers and demultiplexers |